

**REMARKS**

In the office action, claims 32-40 were rejected under 35 U.S.C. §112, first paragraph. The claims have been revised so that the distance determinations and replace with delay determinations. An illustration of delayed determinations is in Figure 8b, range delay 834.

Claims 32-40 were rejected under 35 U.S.C. §102(e) as being anticipated by U.S. Patent No. 5,614,914 and under 35 U.S.C. §103(a) as being unpatentable over U.S. Patent No. 5,365,544 in view of U.S. Patent No. 5,600,706. Neither of these references discloses the synchronization procedure of the revised claims. In the present invention, the mobile receives and despreads a first spread spectrum signal having an associated code. The despread first signal is processed by delay lock loop. The time of the associated code used for despreading the signal is adjusted as well as a clock pulse. The timing of an associated code of the second spread spectrum signal is adjusted with respect to the adjusted timing of the clock pulse and the adjusted timing of the first spread spectrum signal associated code. This synchronization procedure provides for a highly accurate and efficient circuit for forming the chip synchronization. None of the cited references discloses arrangement. Furthermore, with respect to base station, neither references discloses the use of the element of the synchronization process in conjunction with the delay determination as cited in the claims having base station elements, further

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**Application No.: 09/653,057**

distinguishing these claims from the cited references. Accordingly, Applicant respectfully submits that the claims are allowable.

If there are any unresolved issues, the Examiner is respectfully requested to contact the undersigned. Reconsideration and entry of this amendment is respectfully requested.

Respectfully submitted,

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